

Appl. No. 10/801,498
Response to Office Action Dated February 27, 2006

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application.

Listing of Claims:

- 1 1. (Previously Presented) A circuit for operating a transistor as a rectifier, said circuit
2 comprising:
3 a transistor;
4 a control circuit operating said transistor as a function of the Vds voltage potential of
5 said transistor and the on-time or off-time duration of said Vds voltage potential in a present
6 cycle; wherein the on-time and off-time duration of a subsequent cycle is adjusted as a
7 function of said on-time or off-time duration of said Vds voltage potential in said present
8 cycle.
- 1 2. (Currently Amended) A circuit as recited in claim 1 wherein said control circuit
2 further operating said transistor as a function of the on-time or off-time duration of said Vds
3 voltage potential in a previous cycle; and wherein the on-time and off-time duration of a
4 subsequent cycle is further adjusted as a function of on-time or off-time duration of said Vds
5 voltage potential in said previous cycle.
- 1 3. (Cancelled).
- 1 4. (Cancelled).
- 1 5 - 20. Claims 5-20 (Cancelled).

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1 21. (Cancelled).

1 22. (Currently Amended) A circuit for operating a transistor as a rectifier, said circuit
2 comprising:

3 a transistor;

4 a control circuit operating said transistor as a function of the Vds current of said
5 transistor and the on-time or off-time duration of said Vds current in a present cycle;
6 wherein the on-time and off-time duration of a subsequent cycle is adjusted as a function of
7 said on-time or off-time duration of said Vds current in said present cycle.

1 23. (Currently Amended) A circuit as recited in claim 22 wherein said control circuit
2 further operating said transistor as a function of the on-time or off-time duration of said Vds
3 current in a previous cycle; and wherein the on-time and off-time duration of a subsequent
4 cycle is adjusted as a function of said on-time or off-time duration of said Vds current in
5 said previous cycle.

1 24-25. (Cancelled).

1 26. (Currently Amended) A method for operating a transistor as a rectifier, comprising
2 the steps of:

3 detecting the Vds signal of a transistor; and

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4 operating said transistor as a function of the Vds voltage potential of said transistor
5 and the on-time or off-time duration of the Vds voltage potential in a present cycle; and
6 adjusting the on-time and off-time duration of a subsequent cycle as a function of
7 said on-time or off-time duration of said Vds voltage potential in said present cycle.

1 27. (Currently Amended) A circuit as recited in claim 26 wherein, in said operating
2 step, further operating said transistor as a function of the on-time or off-time duration of the
3 Vds voltage potential in a previous cycle; and, in said adjusting step, further adjusting the
4 on-time and off-time duration of a subsequent cycle as a function of said on-time or off-time
5 duration of said Vds voltage potential in said previous cycle.

1 28-29. (Cancelled).

1 30. (Currently Amended) A method for operating a transistor as a rectifier, comprising
2 the steps of:
3 detecting the Vds signal of a transistor; and
4 operating said transistor as a function of the Vds current of said transistor and the on-
5 time or off-time duration of said Vds current in a present cycle and the on-time; and
6 adjusting a subsequent cycle as a function of said on-time and off-time duration of
7 said Vds current in said present cycle.

1 31. (Currently Amended) A circuit as recited in claim 30 wherein, in said operating
2 step, further operating said transistor as a function of the on-time or off-time duration of said

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- 3 Vds current in a previous cycle; and, in said adjusting step, further adjusting the on-time and
4 off-time duration of a subsequent cycle as a function of said on-time or off-time duration of
5 said Vds current in said previous cycle.
- 1 32-33. (Cancelled).